

Energy-aware scheduling of flow applications on master-worker platforms

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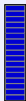
ASTEC, June, 2009

Outline

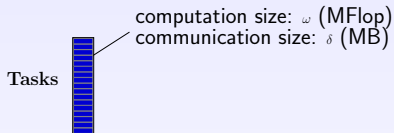
- 1 Framework
 - Application and platform
 - Energy
 - Objective function
- 2 At the processor level
 - Minimizing the power consumption
 - Maximizing the throughput
- 3 At the system level
 - Ideal model
 - Model with start-up overheads
- 4 Conclusion

Applications and platform

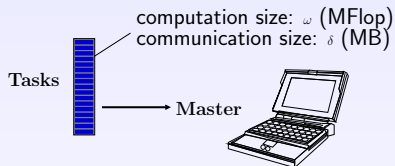
Tasks



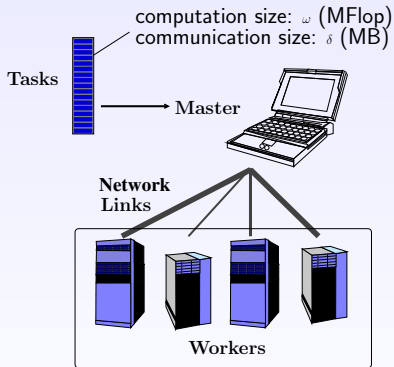
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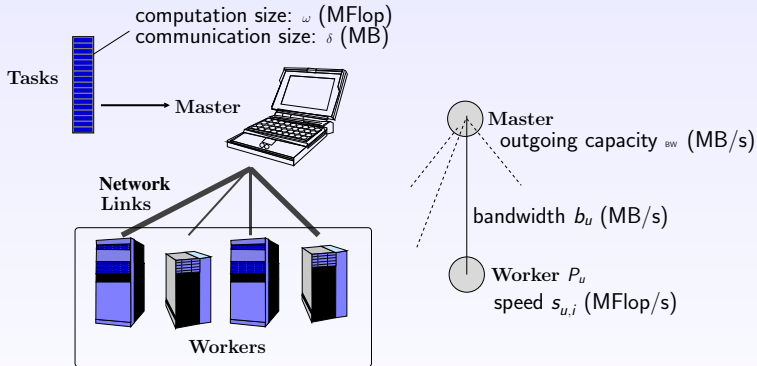
Applications and platform



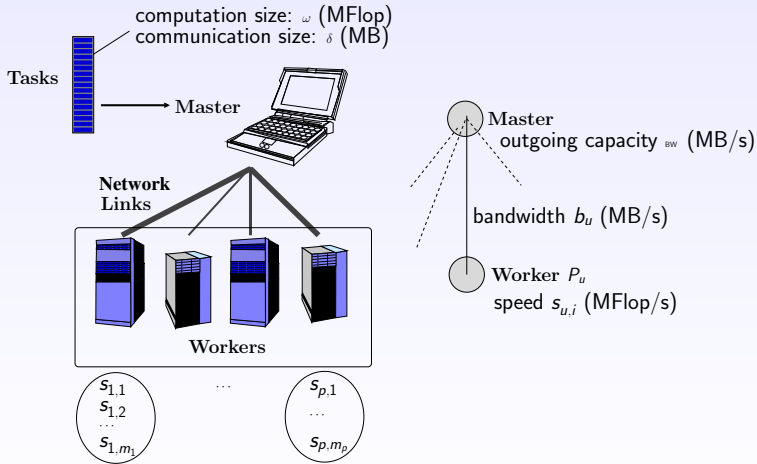
Applications and platform



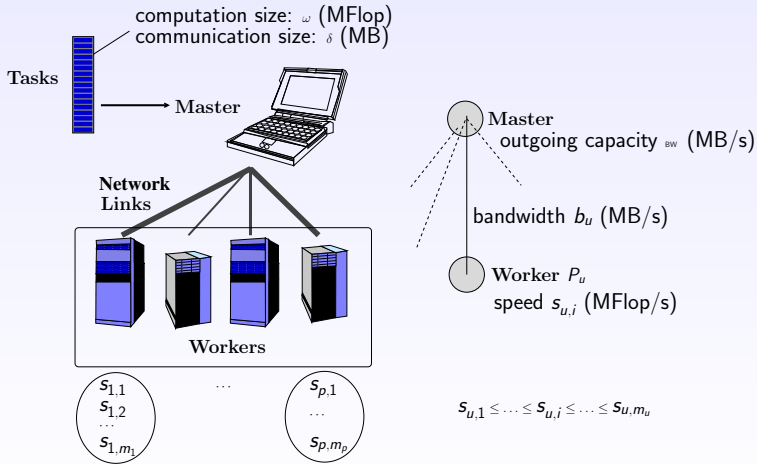
Applications and platform



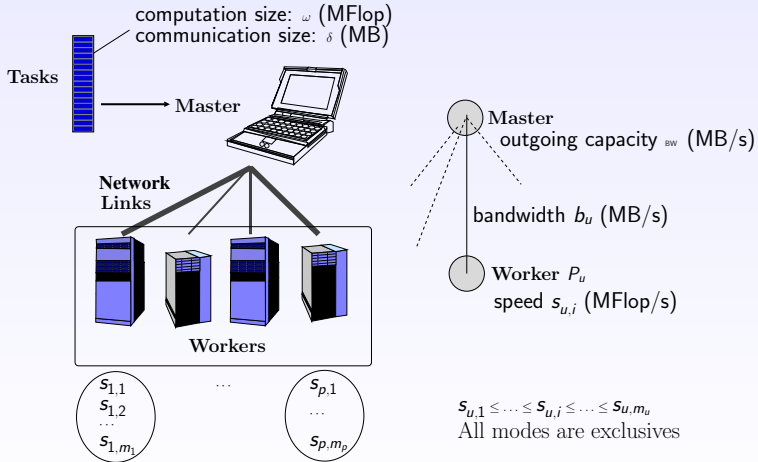
Applications and platform



Applications and platform



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Energy model

- Most common power consumption formula:

$$P_d = s^\alpha, \text{ where } \alpha > 1.$$

- Our hypothesis:

Power consumption is a super-linear function.

- Power consumption of P_u at mode $s_{u,i}$ (fully used): $\mathfrak{P}_{u,i}$

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Between modes

Fluid model:

switching among the modes does not cost any penalty.

Idle:

- an idle processor does not consume any power
- once a processor is on, it will always be above s_{off}

Overhead:

- time overhead
- power overhead
 - when turning on the worker
 - when turning it off
 - for each transition of mode

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- **Ideal model:**
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- **Model with start-up overheads**
 - once a processor is on, it will always be above $s_{u,1}$
 - power consumption depends on the length of the interval

$$\mathfrak{P}_{u,i}(t) = \mathfrak{P}_{u,i}^{(1)} \cdot t + \mathfrak{P}_u^{(2)}$$

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Steady-state

Motivations

- Assume the number of tasks is huge
- Concentrate on *throughput* (fluid framework)
- Assume the workers can run at different speed
- Concentrate on *energy minimization*

Bi-criteria problem

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Bi-criteria problem

Laptop or server ?

Laptop problem:

“What is the best schedule achievable using a particular energy budget, before battery becomes critically low?”

MaxThroughput (\mathfrak{P}): *maximizing the throughput while not exceeding the power consumption \mathfrak{P}*

Server problem:

“What is the least energy required to achieve a desired level of performance?”

MinPower (ρ): *minimizing the power consumption while achieving a throughput ρ*

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Problem

Goal: Minimization of the power consumption of P_u .

Constraints:

- P_u has to ensure a given throughput,
- the processing capacity of $P_{u,i}$ cannot be exceeded, and the different modes are exclusive.

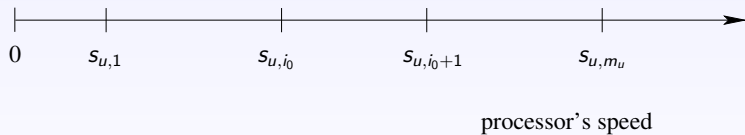
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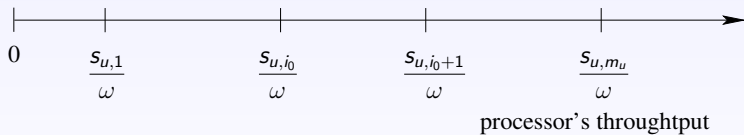
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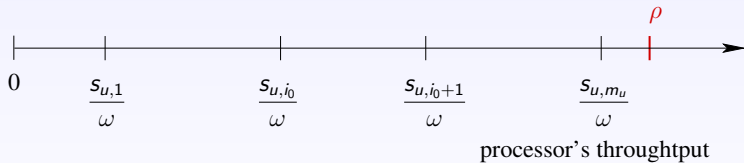
Optimal scheduling



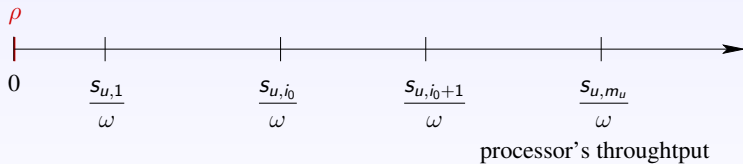
Optimal scheduling



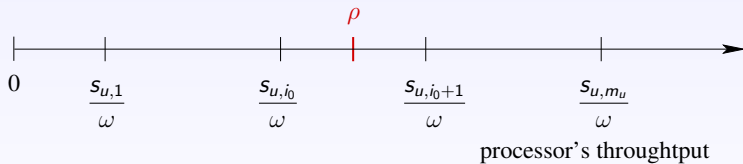
Optimal scheduling



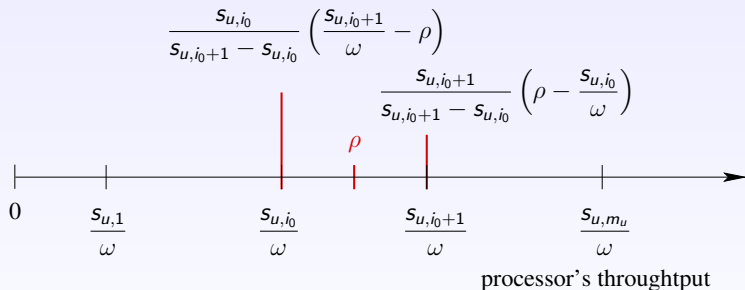
Optimal scheduling



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Optimal scheduling



Theorem 1

Theorem

This scheduling is optimal to minimize the power consumption while achieving a throughput of ρ .

The power consumption is then:

$$\mathfrak{P}_u(\rho) = (\omega\rho - s_{u,i_0}) \frac{\mathfrak{P}_{u,i_0+1} - \mathfrak{P}_{u,i_0}}{s_{u,i_0+1} - s_{u,i_0}} + \mathfrak{P}_{u,i_0}$$

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$$\begin{aligned} \mathfrak{P}_u(\rho) &= (\omega\rho - s_{u,i_0}) \frac{\mathfrak{P}_{u,i_0+1} - \mathfrak{P}_{u,i_0}}{s_{u,i_0+1} - s_{u,i_0}} + \mathfrak{P}_{u,i_0} \\ &= \max_{0 \leq i < m_u} \left\{ (\omega\rho - s_{u,i}) \frac{\mathfrak{P}_{u,i+1} - \mathfrak{P}_{u,i}}{s_{u,i+1} - s_{u,i}} + \mathfrak{P}_{u,i} \right\}. \end{aligned}$$

because \mathfrak{P} is super-linear.

Theorem 2

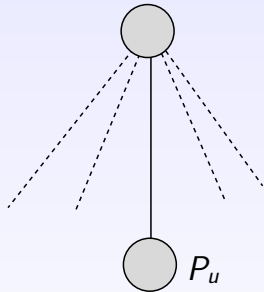
The maximum achievable throughput according to the power consumption limit \mathfrak{P} is

$$\rho_u(\mathfrak{P}) = \min \left\{ \begin{array}{l} \frac{s_{u,m_u}}{\omega} \\ \max_{1 \leq i \leq m_u} \left\{ \frac{\mathfrak{P}(s_{u,i+1} - s_{u,i}) + s_{u,i}\mathfrak{P}_{u,i+1} - s_{u,i+1}\mathfrak{P}_{u,i}}{\omega(\mathfrak{P}_{u,i+1} - \mathfrak{P}_{u,i})} \right\} \end{array} \right\}$$

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Simplifying the problem

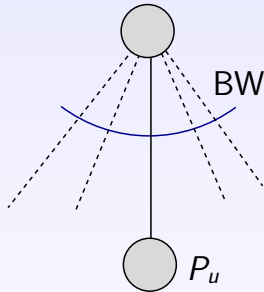


Constraint of the system: ρ

$$\frac{BW}{\delta} \geq \rho, \text{ or the system has no solution.}$$

Use of the previous theorems

Simplifying the problem

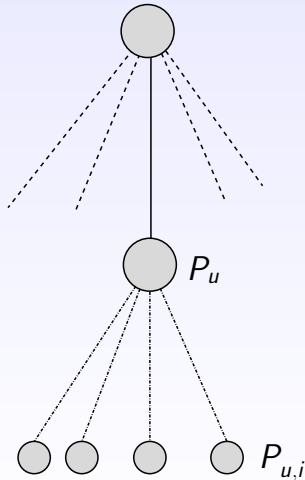


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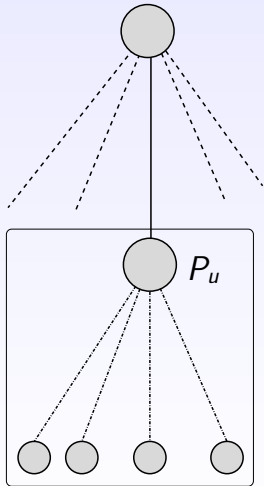


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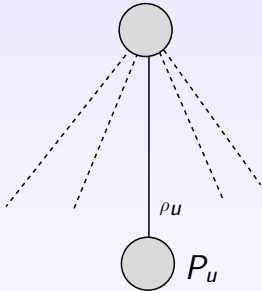


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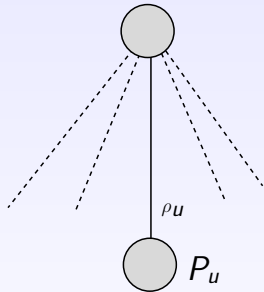


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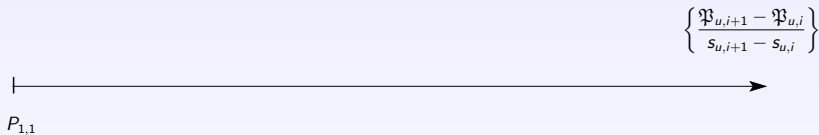
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$$(\omega\rho_u - s_{u,i}) \frac{\mathfrak{P}_{u,i+1} - \mathfrak{P}_{u,i}}{s_{u,i+1} - s_{u,i}} + \mathfrak{P}_{u,i}$$

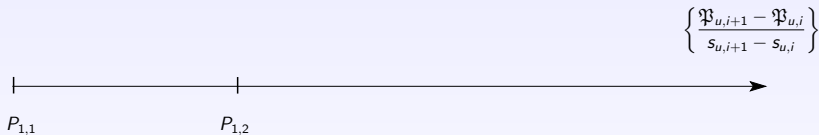
Algorithm

$$\left\{ \frac{\mathfrak{P}_{u,i+1} - \mathfrak{P}_{u,i}}{s_{u,i+1} - s_{u,i}} \right\}$$


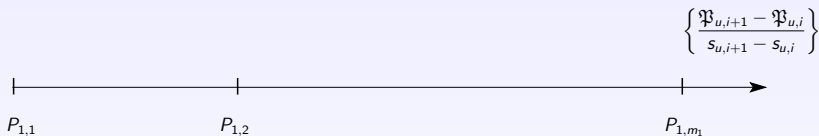
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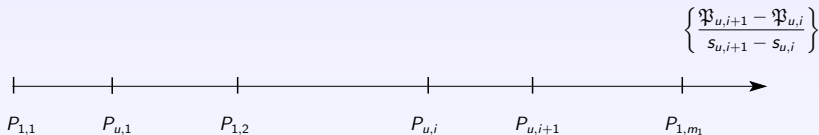
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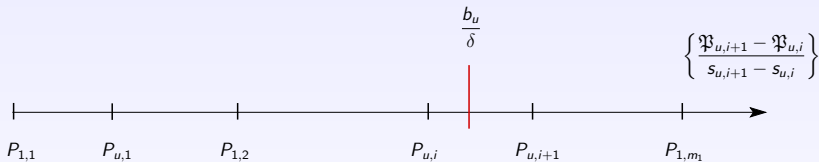
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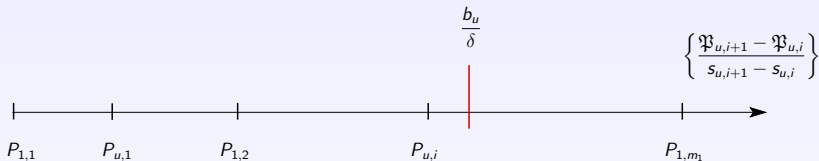
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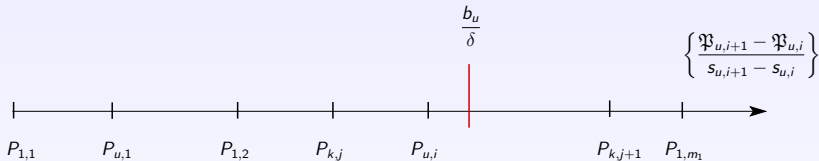
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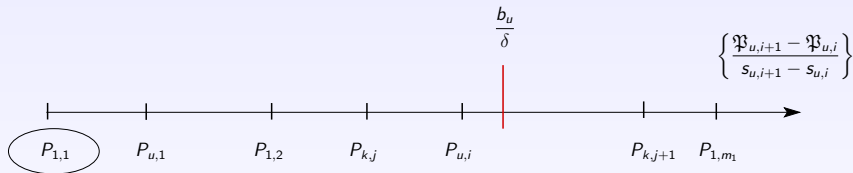
Algorithm



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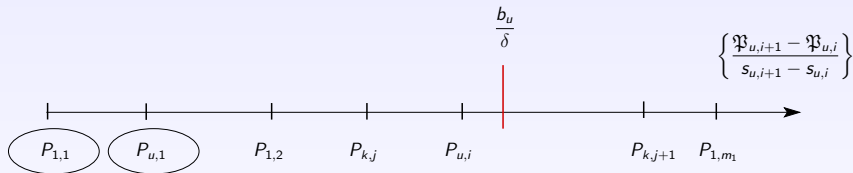


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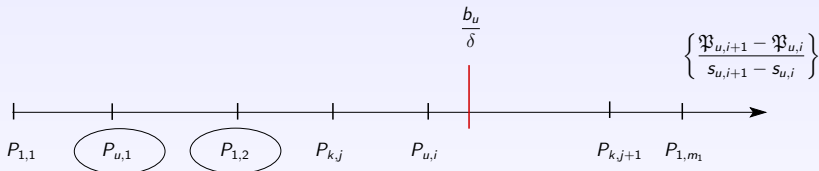
Throughput of the system : $\Phi = \frac{S_{1,1}}{\omega}$

Algorithm



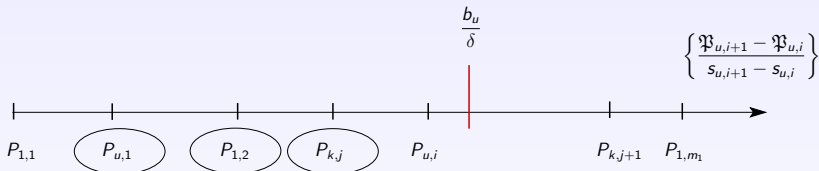
Throughput of the system : $\Phi = \Phi + \frac{S_{u,1}}{\omega}$

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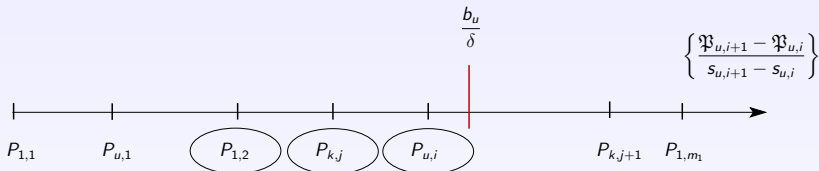
Throughput of the system : $\Phi = \Phi - \frac{S_{1,1}}{\omega} + \frac{S_{1,2}}{\omega}$

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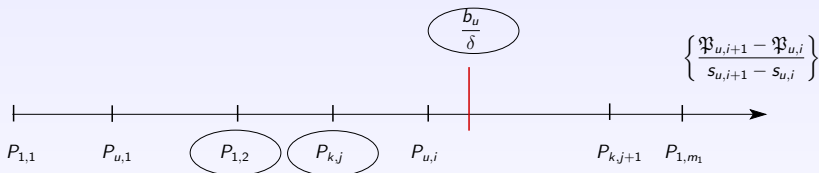
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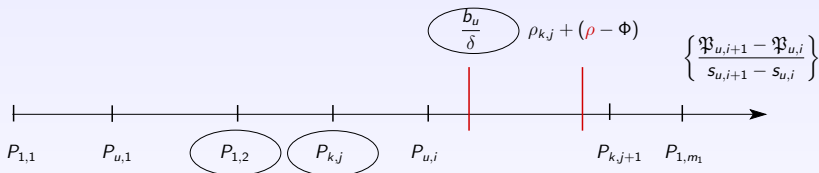
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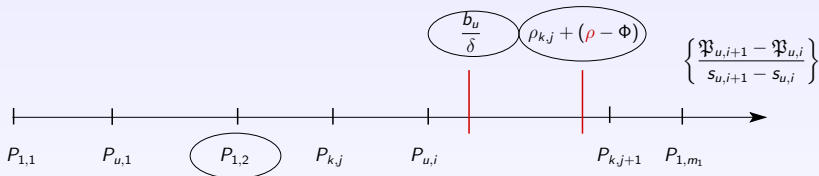


Throughput of the system : $\Phi = \Phi - \frac{s_{u,i}}{\omega} + \frac{b_u}{\delta}$

Algorithm



Algorithm



Throughput of the system : $\Phi = \rho$

Proof of the algorithm

Theorem

Our algorithm optimally solves problem MINPOWER (ρ).

- $\tilde{\mathcal{S}} = \{\tilde{\rho}_u\}$: throughput of each processor given by our algorithm
- $\mathcal{S} = \{\rho_u\}$: another optimal solution

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Proof (1/2)

	$\tilde{\mathcal{S}}$	\mathcal{S}	
P_1	$\tilde{\rho}_1$	ρ_1	
P_u	$\tilde{\rho}_u$	ρ_u	
P_p	$\tilde{\rho}_p$	ρ_p	

Proof (1/2)

	$\tilde{\mathcal{S}}$	\mathcal{S}
P_1	$\tilde{\rho}_1$	ρ_1
	$\tilde{\rho}_{\min}$	ρ_{\min}
	$>$	
P_u	$\tilde{\rho}_u$	ρ_u
P_p	$\tilde{\rho}_p$	ρ_p

Proof (1/2)

	$\tilde{\mathcal{S}}$		\mathcal{S}
P_1	$\tilde{\rho}_1$		ρ_1
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P_u	$\tilde{\rho}_u$		ρ_u
P_p	$\tilde{\rho}_p$		ρ_p

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P_u	$\tilde{\rho}_u$		ρ_u
	$\tilde{\rho}_{\max}$	$<$	ρ_{\max}
P_p	$\tilde{\rho}_p$		ρ_p

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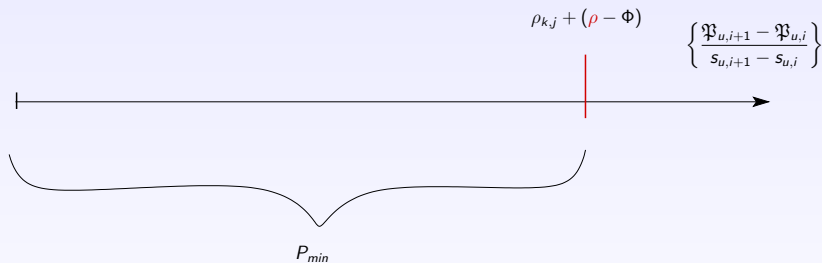
$$\epsilon = \min\{\tilde{\rho}_{\min} - \rho_{\min}; \rho_{\text{MAX}} - \tilde{\rho}_{\text{MAX}}\}.$$

Proof (1/2)

	\tilde{S}	S	S'
P_1	$\tilde{\rho}_1$	ρ_1	ρ_1
P_{\min}	$\tilde{\rho}_{\min}$	$> \rho_{\min}$	$\rho_{\min} + \epsilon$
P_u	$\tilde{\rho}_u$	ρ_u	ρ_u
P_{\max}	$\tilde{\rho}_{\max}$	$< \rho_{\max}$	$\rho_{\max} - \epsilon$
P_p	$\tilde{\rho}_p$	ρ_p	ρ_p

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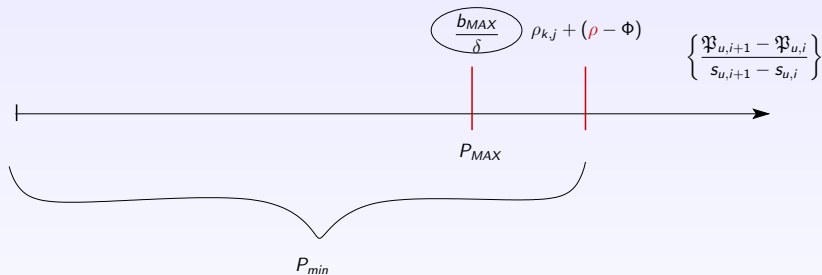
Proof (2/2)



Conclusion

\mathcal{S}' consumes no more power than \mathcal{S}

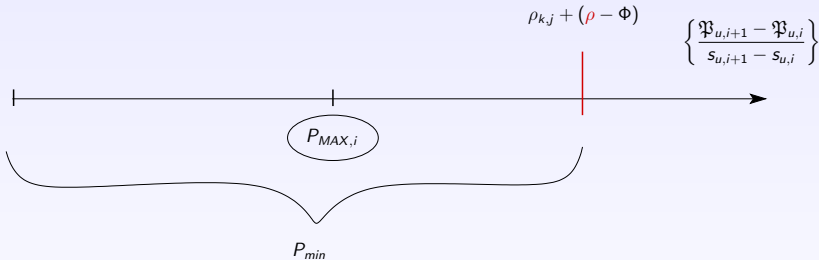
Proof (2/2)



Conclusion

S' consumes no more power than S

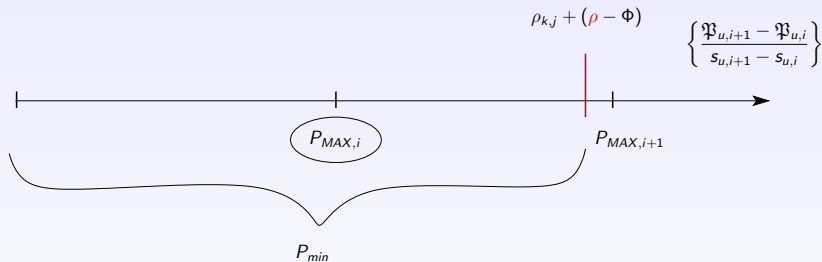
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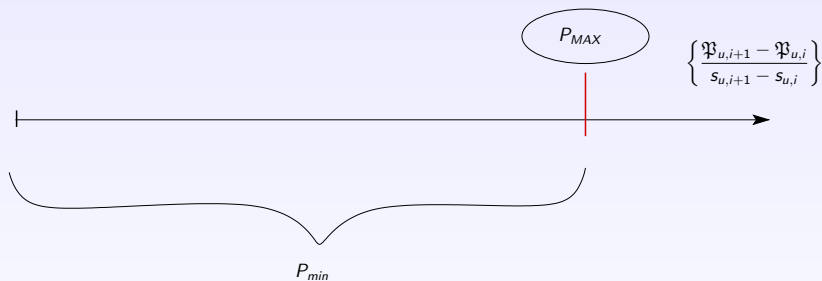
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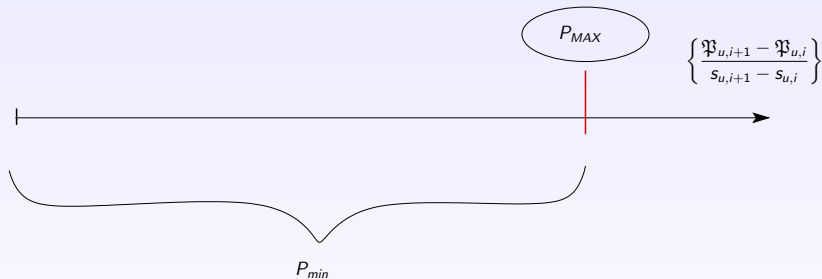
Proof (2/2)



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Proof (2/2)



Conclusion

\mathcal{S}' consumes no more power than \mathcal{S}

Maximizing the throughput

- Same principle
- Simple change of objective function:

$$\mathcal{T}[u_k] \leftarrow \min \begin{cases} \mathfrak{P}_{u_k, i_k} \\ \left(\omega \frac{b_{u_k}}{\delta} - s_{u_k, i_k} \right) \frac{\mathfrak{P}_{u_k, i_{k+1}} - \mathfrak{P}_{u_k, i_k}}{s_{u_k, i_{k+1}} - s_{u_k, i_k}} + \mathfrak{P}_{u_k, i_k} \\ \mathfrak{P}' + (\mathfrak{P} - \Psi) \end{cases}$$

(Ψ is the current power consumption)

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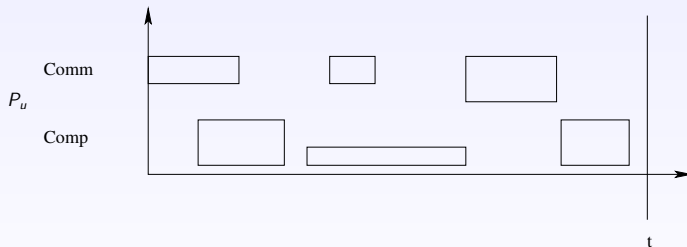
Property

Theorem

There exists an optimal schedule in which all processors, except possibly one, are used at a maximum throughput, i.e., either the throughput dictated by their bandwidth, or the throughput achieved by one of their execution modes.

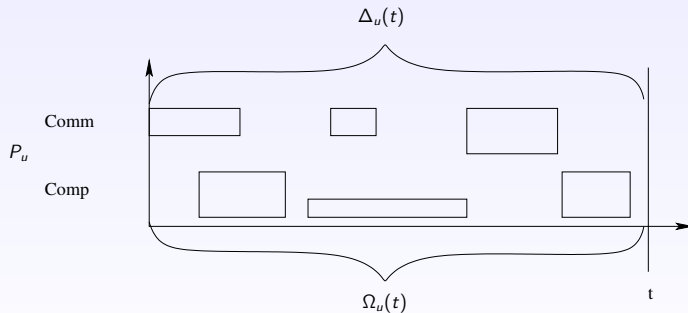
Proof

\mathcal{S} , optimal schedule, during t time-units:



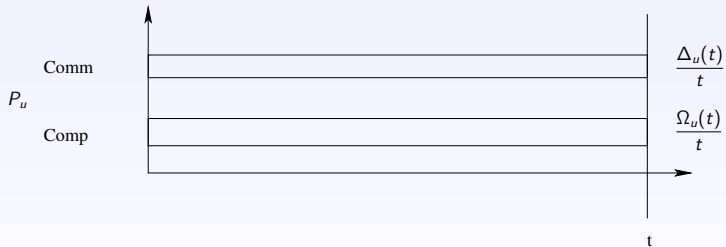
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Proof

S' :



\mathcal{S}' optimal

If \mathcal{S}' does not have the desired property:

- P_{\min}, P_{\max} : not running at a maximum throughput.
- $\rho'_{\min} \leftarrow P_{\min, i_{\min}}, P_{\min, i_{\min}+1}$
- $\rho'_{\max} \leftarrow P_{\max, i_{\max}}, P_{\max, i_{\max}+1}$
-

$$\frac{\mathfrak{P}_{\min, i_{\min}+1}(t) - \mathfrak{P}_{\min, i_{\min}}(t)}{s_{\min, i_{\min}+1} - s_{\min, i_{\min}}} \leq \frac{\mathfrak{P}_{\max, i_{\max}+1}(t) - \mathfrak{P}_{\max, i_{\max}}(t)}{s_{\max, i_{\max}+1} - s_{\max, i_{\max}}}$$

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- $\rho'_{\text{MAX}} \leftarrow P_{\text{MAX}, i_{\text{MAX}}}, P_{\text{MAX}, i_{\text{MAX}}+1}$
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$$\frac{\mathfrak{P}_{\min, i_{\min}+1}(t) - \mathfrak{P}_{\min, i_{\min}}(t)}{s_{\min, i_{\min}+1} - s_{\min, i_{\min}}} \leq \frac{\mathfrak{P}_{\text{MAX}, i_{\text{MAX}}+1}(t) - \mathfrak{P}_{\text{MAX}, i_{\text{MAX}}}(t)}{s_{\text{MAX}, i_{\text{MAX}}+1} - s_{\text{MAX}, i_{\text{MAX}}}}$$

New schedule

$$\mathcal{S}'' : \begin{cases} \rho''_{\min} = \rho'_{\min} + \epsilon \\ \rho''_{\max} = \rho'_{\max} - \epsilon \\ \rho''_u = \rho'_u \text{ otherwise} \\ \epsilon = \min \left\{ \frac{b_u}{\delta} - \rho'_{\min}; \frac{S_{\min, i_{\min}+1}}{\omega} - \rho'_{\min}; \rho'_{\max} - \frac{S_{\max, i_{\max}}}{\omega} \right\} \end{cases}$$

\mathcal{S}''

- achieves the same throughput than \mathcal{S}'
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Asymptotic optimality (1/2)

- Algorithm look at the energy consumed during d time-units (d defined later)
- \mathcal{A} is composed of \mathcal{B} tasks
- optimal scheduling time: $T = \frac{\mathcal{B}}{\rho}$, where ρ is the throughput bound.
- \mathfrak{P}_{opt} : the optimal power consumption that would be obtained in the ideal model
- \mathfrak{P}^* : the optimal power consumption that can be achieved under the model with start-up overheads
- \mathfrak{P} the power consumption given by our algorithm.
- $\mathfrak{P}_{opt} \leq \mathfrak{P}^* \leq \mathfrak{P}$.
- during t time-units:

$$\mathfrak{P}(t) \leq \mathfrak{P}_{opt} \cdot t + \left\lceil \frac{t}{d} \right\rceil \sum_{u=1}^p \mathfrak{P}_u^{(2)}$$

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Asymptotic optimality (2/2)

If we fix $d = \sqrt{T}$, we have

$$\mathfrak{P}(T) \leq \mathfrak{P}^* \cdot T + (1 + \sqrt{T}) \cdot p \cdot \max_{u=1}^p \left\{ \mathfrak{P}_u^{(2)} \right\}.$$

Then, when comparing \mathfrak{P} and \mathfrak{P}^* during the scheduling of the \mathcal{B} tasks of application \mathcal{A} , we obtain:

$$\begin{aligned} \frac{\mathfrak{P}(T)}{\mathfrak{P}^*(T)} &\leq 1 + \left(\frac{1}{T} + \frac{1}{\sqrt{T}} \right) \frac{p \cdot \max_{u=1}^p \left\{ \mathfrak{P}_u^{(2)} \right\}}{\mathfrak{P}^*} \\ &\leq 1 + \mathcal{O}\left(\frac{1}{\sqrt{T}}\right). \end{aligned}$$

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- add memory constraints
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